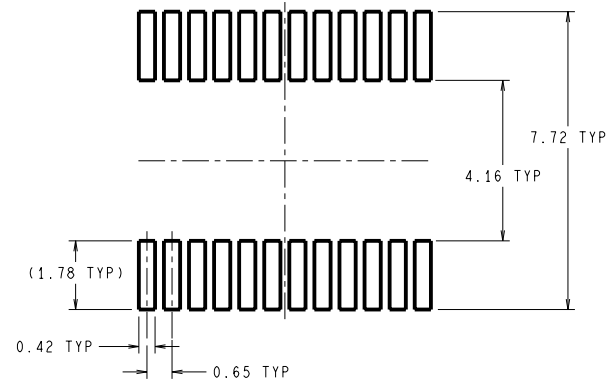
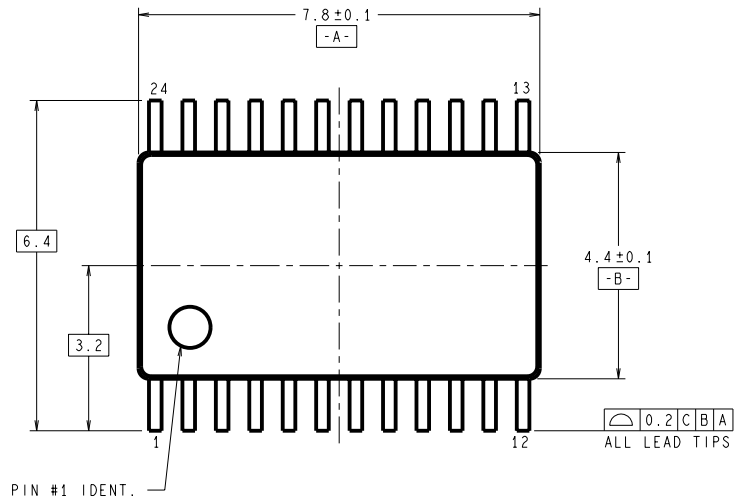
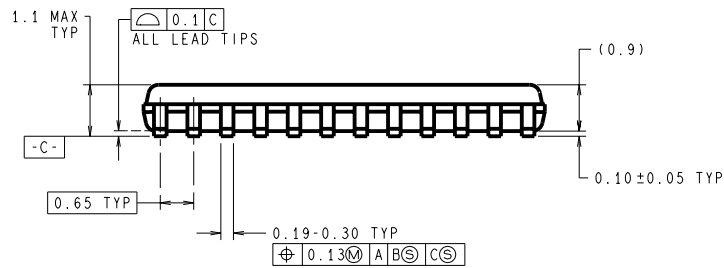


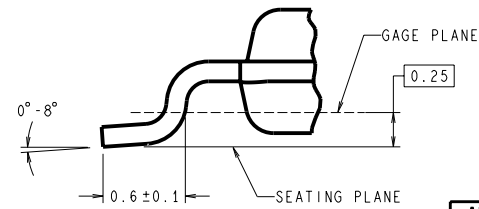
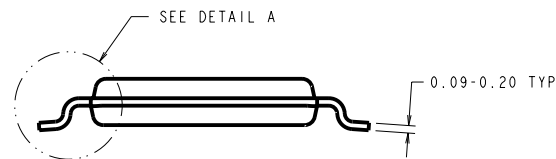
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
C	REVISE & REDRAW ON PRO/E PER CURRENT STD; CORRECT DET CALLOUT FROM D TO A.	11106	08/30/95	MS/



LAND PATTERN RECOMENDATION



DIMENSIONS ARE IN MILLIMETERS



DETAIL A
TYPICAL, SCALE: 40X

NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION AD, DATED 7/93.

APPROVALS		DATE	National Semiconductor		
DRAWN	MARTA SUCHY	08/30/95	2900 Semiconductor Dr., Santa Clara, CA 95052-8090		
DFTG. CHK.			MOLDED TSSOP, JEDEC, 4.4mm BODY WIDTH, 24 LD, 0.65mm PITCH		
ENGR. CHK.					
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	MKT-MTC24	C
DO NOT SCALE DRAWING SHEET 1 of 1					