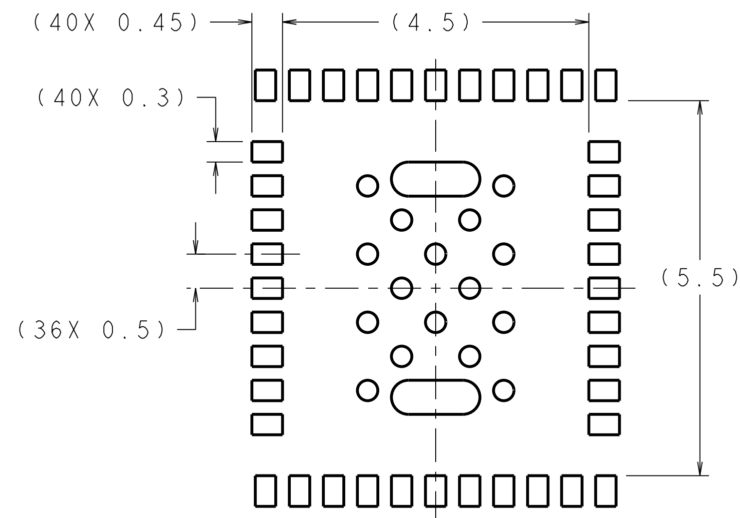
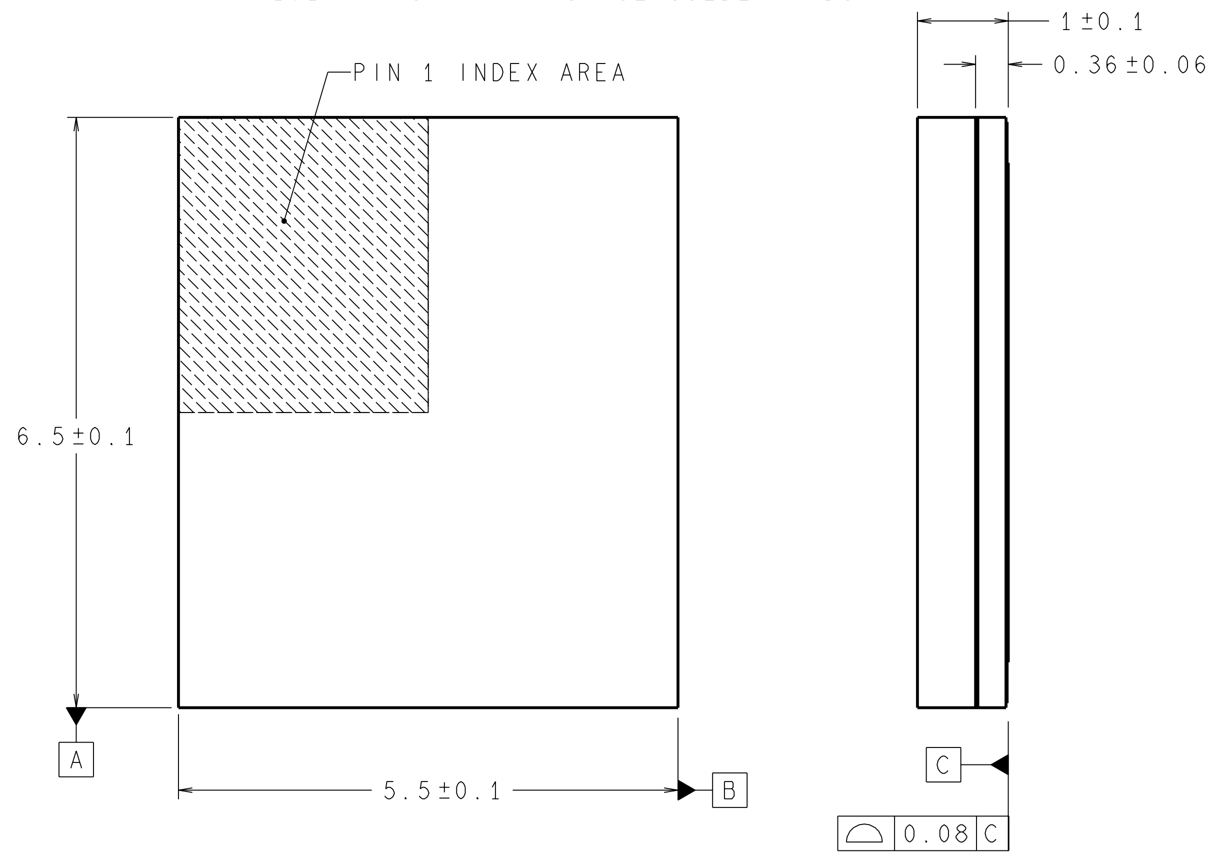
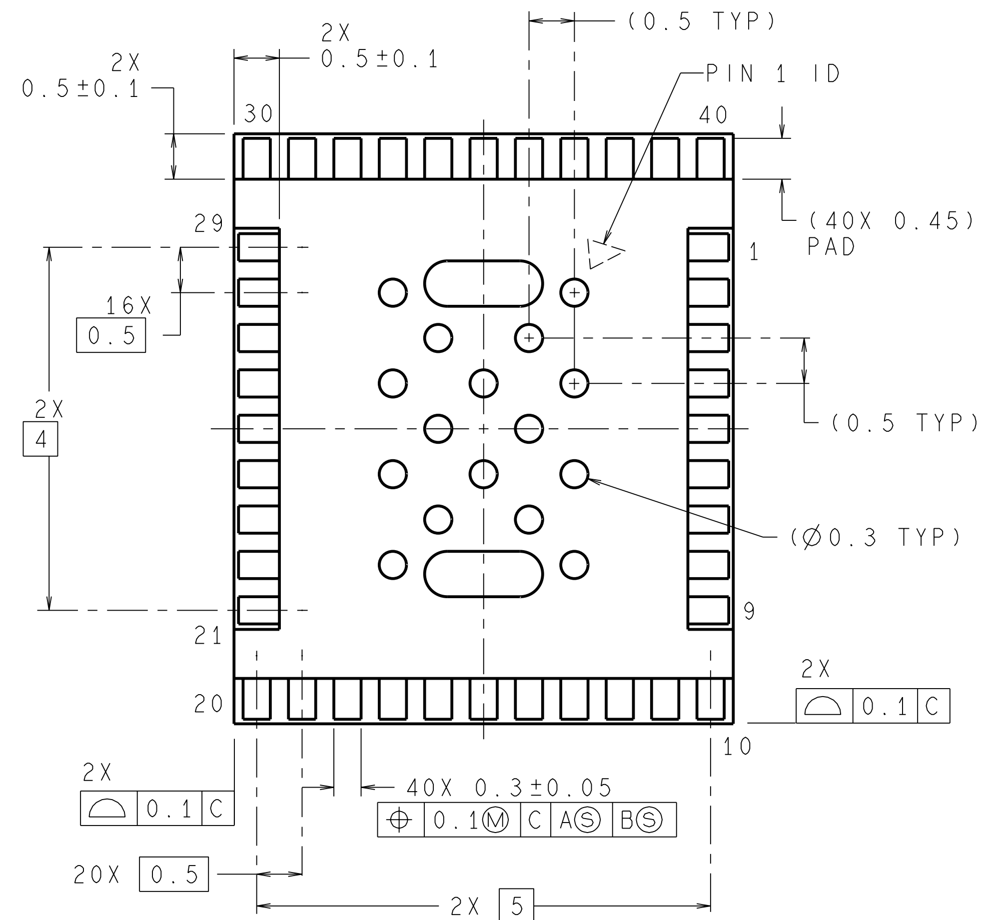
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12377	03/01/2000	TL/TF



RECOMMENDED LAND PATTERN

1:1 RATIO WITH PACKAGE SOLDER PADS





DIMENSIONS ARE IN MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

1. MATERIAL: BT RESIN CCL-HL832 WITH TAIYO PSR4000 AUS5 SOLDER MASK.
2. PLATING: Cu 15 TO 20 MICROMETERS (FULL)
   Ni 10 ± 5 MICROMETERS (LEADS ONLY)
   Au 1 ± 0.5 MICROMETER (LEADS ONLY)
3. REFERENCE JEDEC MO-208, VARIATION KFEA, DATED DECEMBER 1999.

APPROVALS	DATE
DRAWN T. LEQUANG	03/01/2000
DFTG. CHK. MARTA SUCHY	03/01/2000
ENGR. CHK. TONYA FRIDLYAND	03/01/2000

National Semiconductor
2900 Semiconductor Dr, Santa Clara, CA 95052-8090

CSP, PLASTIC, LAMINATED, 6.5 X 5.5 X 1.0 mm BODY, 40 L, 0.5 mm PITCH

PROJECTION
INCH [MM]

SCALE	SIZE	DRAWING NUMBER	REV
N/A	C	(SC)MKT-SLB40A	A

FORMERLY: N/A

SHEET 1 of 1